

10 11 Timeslot X LC X Bit1 LC X Bit2 LC Y Bit3 LC Y Timeslot Y Bit4 LC Y Bit5 Bit6 LC Y LC Z Bit7 Timeslot Z LC Z Bit8

FIG. 1b (Prior Art)

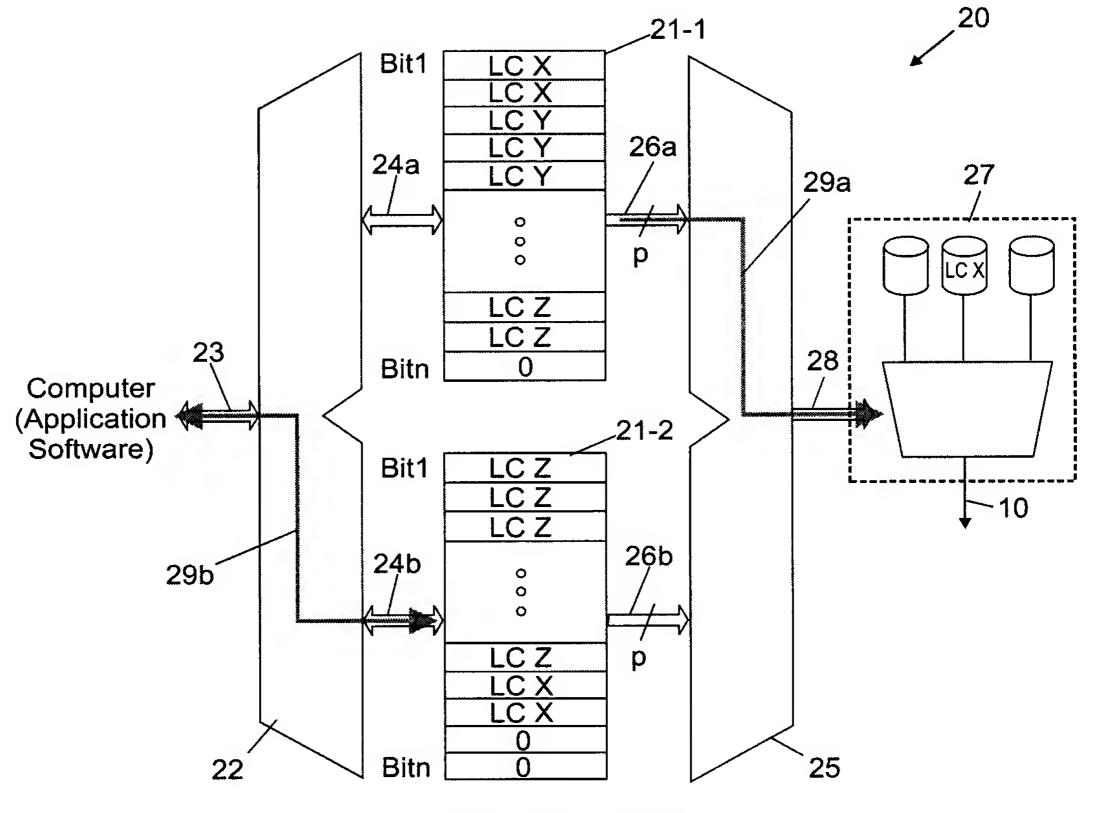


FIG. 2 (Prior Art)

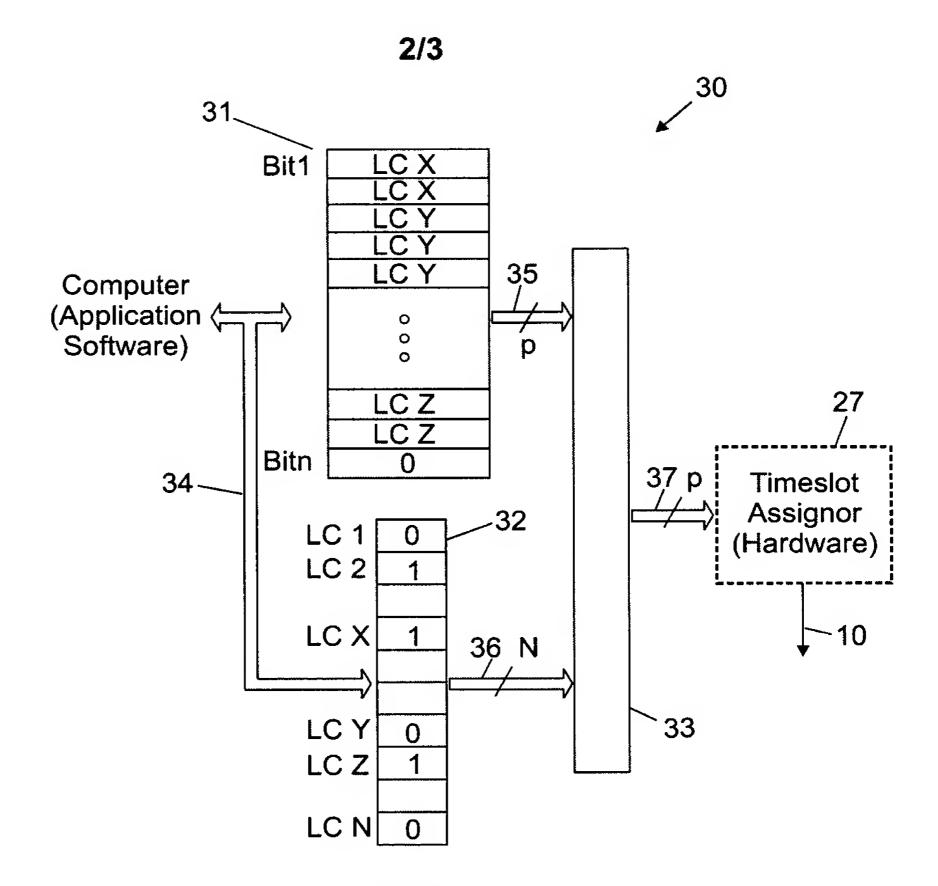


FIG. 3

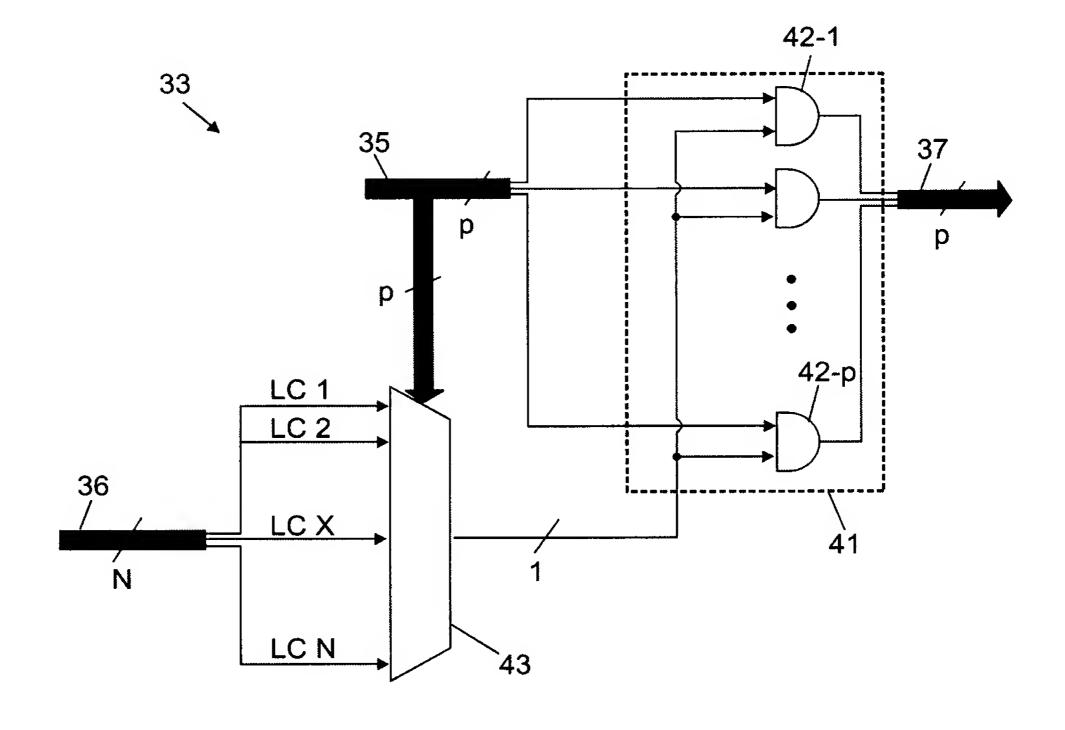


FIG. 4

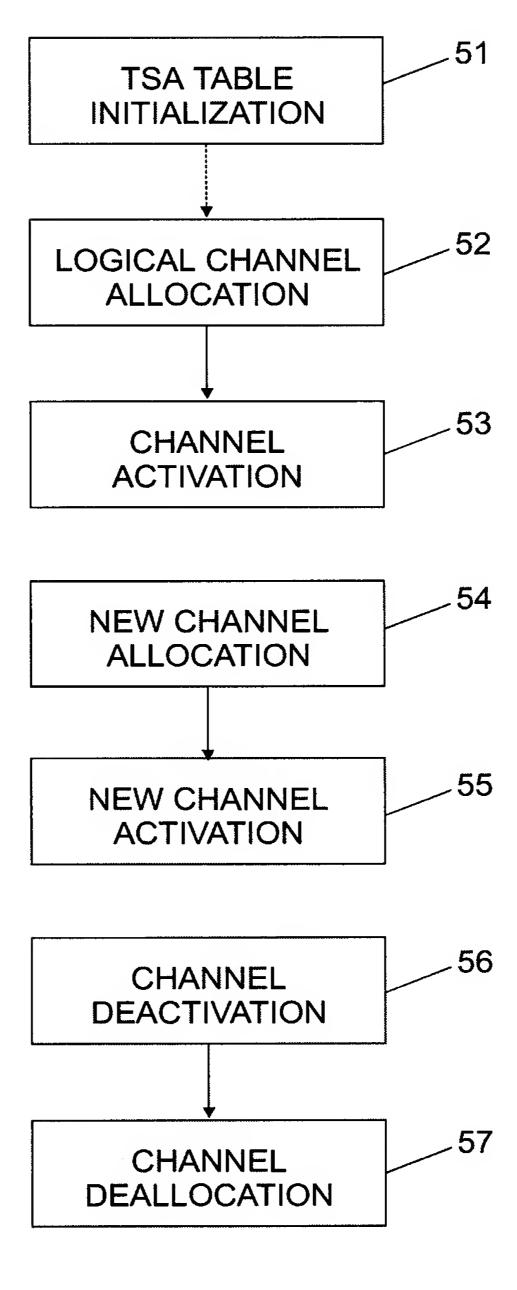


FIG. 5